



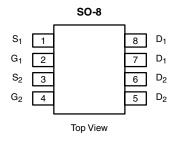
# **Dual N-Channel 75-V (D-S) MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
75	0.048 at V <sub>GS</sub> = 10 V	4.8		
	0.062 at V <sub>GS</sub> = 4.5 V	4.2		

### **FEATURES**

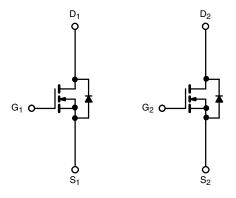
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- 175 °C Maximum Junction Temperature
- High-Efficiency PWM Optimized
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4992EY-T1-E3 (Lead (Pb)-free)

Si4992EY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_A = 25$ °C, unles	ss otherwise i	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	75		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		V
Continuous Dunin Courset /T 475 90\8	T <sub>A</sub> = 25 °C	I <sub>D</sub>	4.8	3.6	
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		3.7	2.8	
Continuous Source Current <sup>a</sup>		I <sub>S</sub>	2	1.1	Α
Pulsed Drain Current		I <sub>DM</sub>	20		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	8		
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mm	E <sub>AS</sub>	3.2		mJ
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.4	1.4	W
	T <sub>A</sub> = 85 °C		1.4	0.8	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Mariana landia la Andria la	t ≤ 10 s	R <sub>thJA</sub>	50	62.5	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		85	110		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	31	37		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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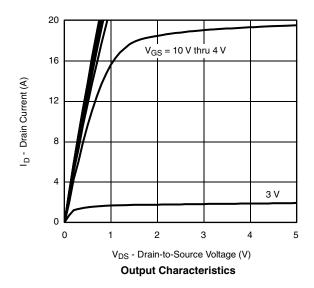
Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit	
Static			<u>'</u>	1			
Gate Threshold Voltage V <sub>GS(</sub>		$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	<sub>aS</sub> = ± 20 V		± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 0 V			1	4	
		$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			20	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-State Resistance <sup>a</sup>	р	$V_{GS} = 10 \text{ V}, I_D = 4.8 \text{ A}$		0.039	0.048	0	
	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 4.2 \text{ A}$		0.050	0.062	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 4.8 \text{ A}$		16		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.4 A, V <sub>GS</sub> = 0 V		0.8	1.2	٧	
Dynamic <sup>b</sup>	,		<u>'</u>				
Total Gate Charge	$Q_g$			14	21		
Gate-Source Charge	$Q_{gs}$ $V_{DS} = 38 \text{ V}, V_{GS} = 10 \text{ V},$	$V_{DS} = 38 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 4.8 \text{ A}$		2.4		nC	
Gate-Drain Charge	Q <sub>gd</sub>			3.5			
Gate Resistance	$R_g$	f = 1 MHz		3.6		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			7	15		
Rise Time	$\begin{array}{c} t_r \\ \\ t_{d(off)} \end{array} \hspace{0.2in} V_{DD} = 38 \text{ V, } R_L = 38 \Omega \\ I_D \cong 1 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 6 \Omega \end{array}$		10	15	ns		
Turn-Off Delay Time			22	35			
Fall Time	t <sub>f</sub>			10	15		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.4 A, dI/dt = 100 A/μs		25	50		

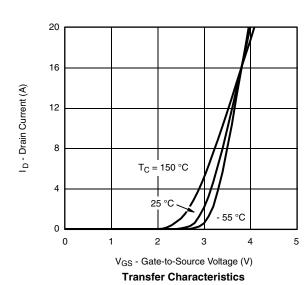
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



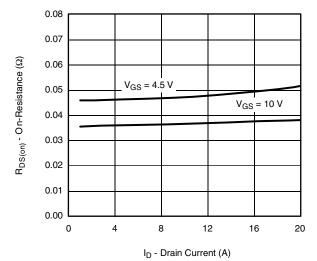




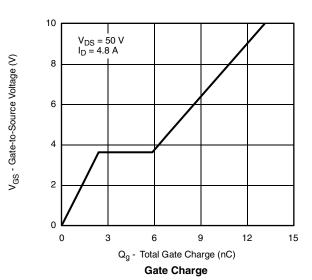


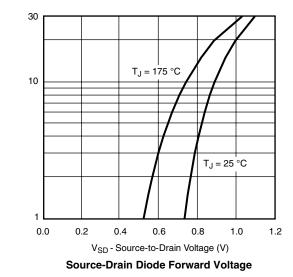


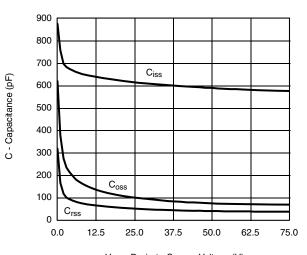
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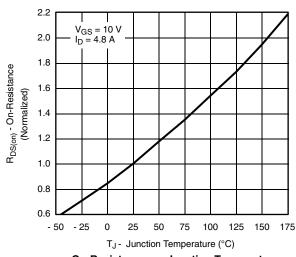
**On-Resistance vs. Drain Current** 



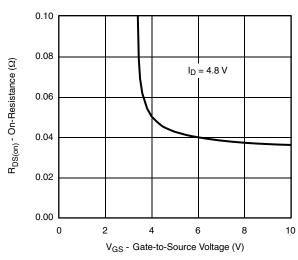




 $V_{DS}$  - Drain-to-Source Voltage (V) **Capacitance** 



On-Resistance vs. Junction Temperature



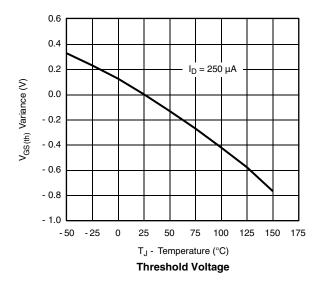
On-Resistance vs. Gate-to-Source Voltage

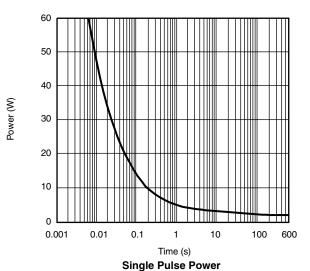
Is - Source Current (A)

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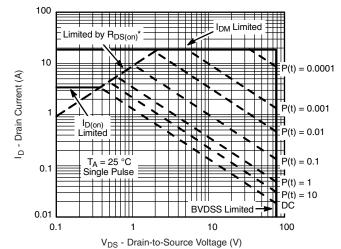
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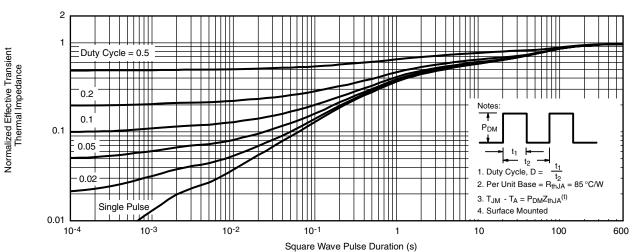


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 $^{\star}$   $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

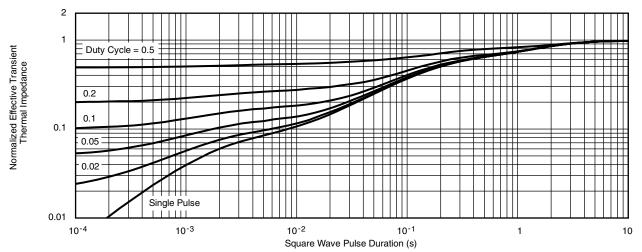
### Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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