Test Procedure for the LV52206XAEVB Evaluation Board

Input Voltage	2.7~5.5V
Over Voltage Protection	38V
Oscillation Frequency	600kHz

1. Please input "High" into SWIRE PIN during the period that is longer than Ton(20uSec) to start IC.

2. Then, please select a mode during mode select period(Tsel) .When you select Digital Mode, please input "Low" longer than Tw1 after "High" longer than Tw0(100uSec) within Tsel(1mSec) period. It becomes PWM mode if you fail to set Digital mode in specified timing period.

   In the case of PWM frequency is less than 6.6kHz, it may become Digital Mode when you input a narrow pulse of Duty.To evade it, input "High" that is longer than Tsel(2.2mSec), and, please input PWM pulse afterwards.

3. IC shut down when you make SWIRE PIN Low longer than Toff(8.9ms) period.

   The Data register is stored at this point. The reset of the power supply is necessary to clear it. In addition, the mode is initialized when you shut down IC. Please make mode select each time you reboot.
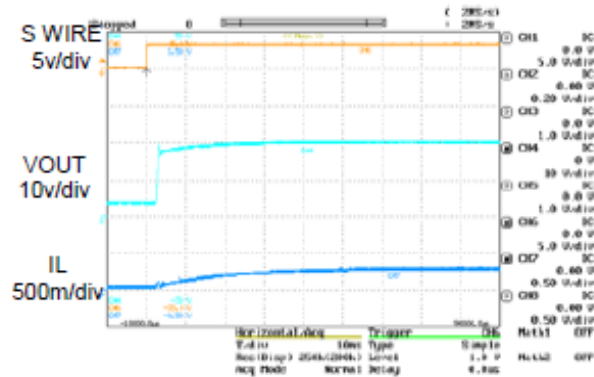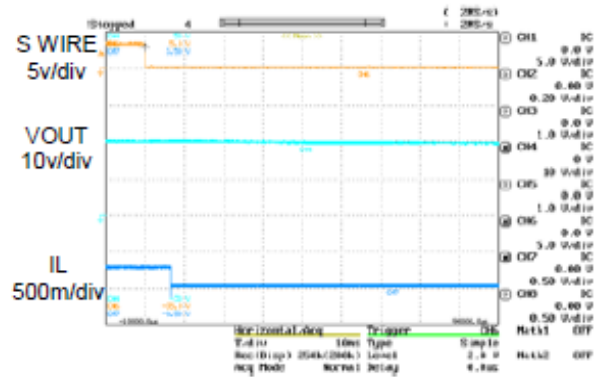
Digital Mode
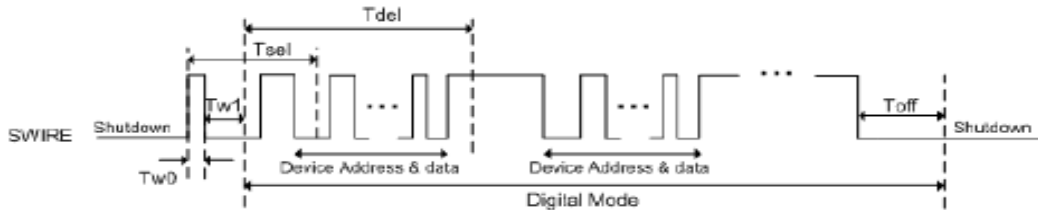
PWM Mode
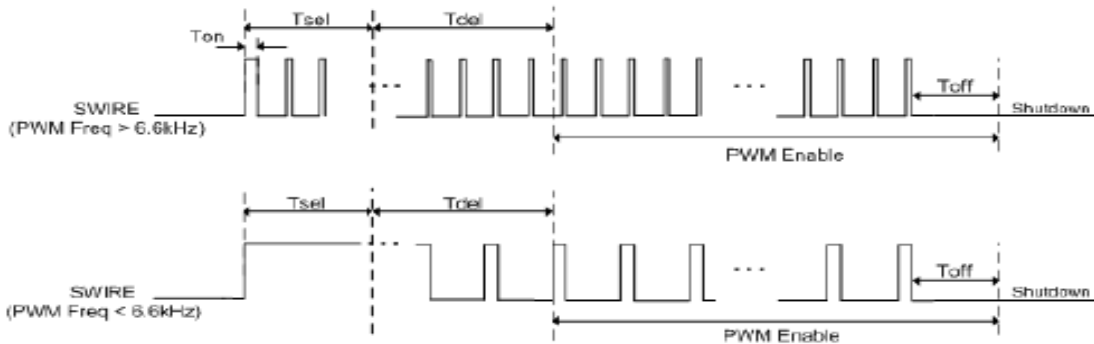
Start Up

VIN=3.6V, L=22uH, 6LED*2para Cfcap=22nF LEDI=20

Shutdown

VIN=3.6V, L=22uH, 6LED*2para Cfcap=22nF LEDI=20mA

BITMAP of the LED Control

adress		R/W	DATA				
A1	A0		D4	D3	D2	D1	D0
0	0	W	LEDI[4:0]				
			1	1	0	0	0
0	1	W	-	-	-	-	OVP
			0	0	0	0	0
1	0	W	-	-	-	LED2OFF	LED1OFF
			0	0	0	0	0

Upper column : Register name Lower column : Default value