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Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.43$ V at $I_F = 5$ A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 30 A				
V _{RRM}	120 V				
I _{FSM}	300 A				
V_F at I_F = 30 A (T_A = 125 °C)	0.69 V				
T _J max.	175 °C				
Package	TO-220AB				
Diode variations	Dual common cathode				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN Solder dip 275 °C max. 10 s, per JESD 22-B106
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60M120C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	120	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	60		
	per diode		30	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	300		
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +175	°C	



RoHS COMPLIANT

FREE





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ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F (1)	0.51	-	V	
	I _F = 15 A			0.68	-		
	I _F = 30 A			0.86	0.97		
	I _F = 5 A	T _A = 125 °C		0.43	-		
	I _F = 15 A			0.58	-		
	I _F = 30 A			0.69	0.77		
Reverse current per diode	V _R = 90 V	T _A = 25 °C	I _R ⁽²⁾	75	-	μA	
		T _A = 125 °C		6.4	-	mA	
	V = 120 V	$T_{A} = 25 \text{ °C}$		-	500	μA	
	$V_{\rm R} = 120 \text{ V}$ $T_{\rm A} = 125 ^{\circ}$	T₄ = 125 °C		10	35	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60M120C	UNIT	
	per diode	R _{θJC}	1.0	°C/W	
Typical thermal resistance ⁽¹⁾	per device		0.7		
	per device		52		

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient dP_D/dT_J < 1/R_{θ JA}

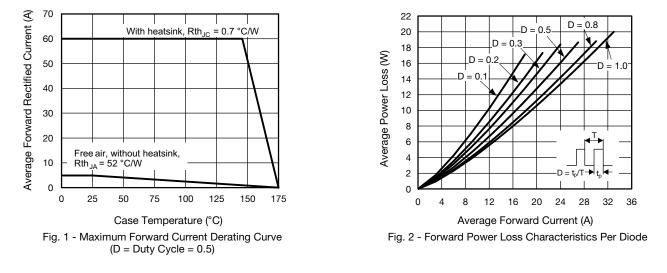
⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	V60M120C-M3/4W	1.89	4W	50/tube	Tube	
TO-220AB	V60M120CHM3/4W (1)	1.89	4W	50/tube	Tube	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

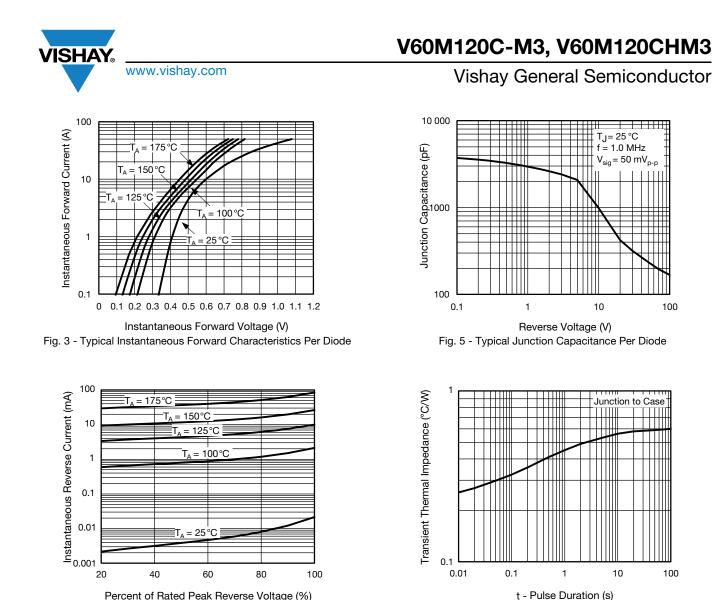


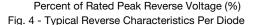
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



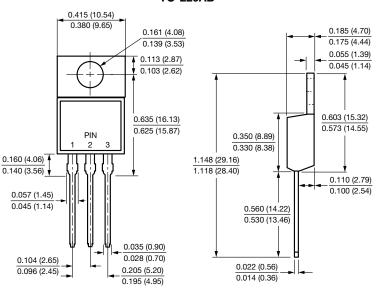


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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