

Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY					
	V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)	
N-Channel	40	0.039 at V _{GS} = 10 V	6.6	6.6	
		0.050 at $V_{GS} = 4.5 \text{ V}$	5.8	0.0	

FEATURES

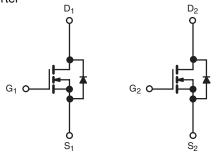
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



COMPLIANT
HALOGEN
FREE
Available

APPLICATIONS

· CCFL Inverter



N-Channel MOSFET

N-Channel MOSFET

	SO-8	_	
S ₁ 1		8	D ₁
G ₁ 2		7	D_1
S ₂ 3		6	D_2
G ₂ 4		5	D_2
ı	Top View	_	

Ordering Information: Si4906DY-T1-E3 (Lead (Pb)-free)

Si4906DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	40	V	
Gate-Source Voltage	V _{GS}	± 16	v	
	T _C = 25 °C		6.6	
Continuous Proin Current /T = 150 °C\	T _C = 70 °C		5.3	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	5.3 ^{b, c}	
	T _A = 70 °C		4.2 ^{b, c}	
Pulsed Drain Current (10 µs Pulse Width)	I _{DM}	30	А	
, ,	T _C = 25 °C	,	2.5	
Source-Drain Current Diode Current	T _A = 25 °C	- I _S	1.7 ^{b, c}	
Pulsed Sorce-Drain Current	I _{SM}	30		
Single Pulse Avalanche Current		I _{AS}	13	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	8.5	mJ
	T _C = 25 °C		3.1	
Manipus David Distriction	T _C = 70 °C		2	
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^{b, c}	W
	T _A = 70 °C		1.28 ^{b, c}	
Operating Junction and Storage Temperature Rang	T _J , T _{stq}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
		Liı				
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	52	62.5	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	32	40	C/VV	

Notes:

- a. Based on T_{C} = 25 $^{\circ}C.$
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under Steady State conditions is 110 $^{\circ}\text{C/W}.$



Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		40		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 4.6			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8		2.2	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 16 \text{ V}$			100	nA	
Zero Gate Voltage Drain Current	,	V _{DS} = 40 V, V _{GS} = 0 V			1	μΑ	
Zero Gate voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-State Resistance ^b	D	$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		0.032	0.039	Ω	
	H _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 4 \text{ A}$		0.041	0.050		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 5 A		15		S	
Dynamic ^a							
Input Capacitance	C _{iss}			625		pF	
Output Capacitance	C _{oss}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		88			
Reverse Transfer Capacitance	C _{rss}			50			
Total Gate Charge	Qg	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		14.4	22	nC	
		$V_{DS} = 20 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		6.6	10		
Gate-Source Charge	Q_{gs}			1.6			
Gate-Drain Charge	Q_{gd}			2.3			
Gate Resistance	R_g	f = 1 MHz		2.3	3.5	Ω	
Turn-On Delay Time	t _{d(on)}			9	15		
Rise Time	t _r	$V_{DD} = 20 \text{ V}, R_L = 4 \Omega$		51	77	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		21	32		
Fall Time	t _f			6	10		
Turn-On Delay Time	t _{d(on)}			13	20		
Rise Time	t _r	$V_{DD} = 20 \text{ V}, R_L = 4 \Omega$		85	128		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		17	26		
Fall Time	t _f			7	11	1	
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			2.5	۸	
Pulse Diode Forward Current ^a	I _{SM}				30	Α	
Body Diode Voltage	V _{SD}	I _S = 1.7 A		0.79	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 1.7 A, dl/dt = 100 A/μs, T _J = 25 °C		30	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			30	45	nC	
Reverse Recovery Fall Time	t _a	1 1F = 1.7 A, αί/αι = 100 A/μs, 1J = 25 °C		17		no	
Reverse Recovery Rise Time	t _b			13		ns	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Guaranteed by design, not subject to production testing.

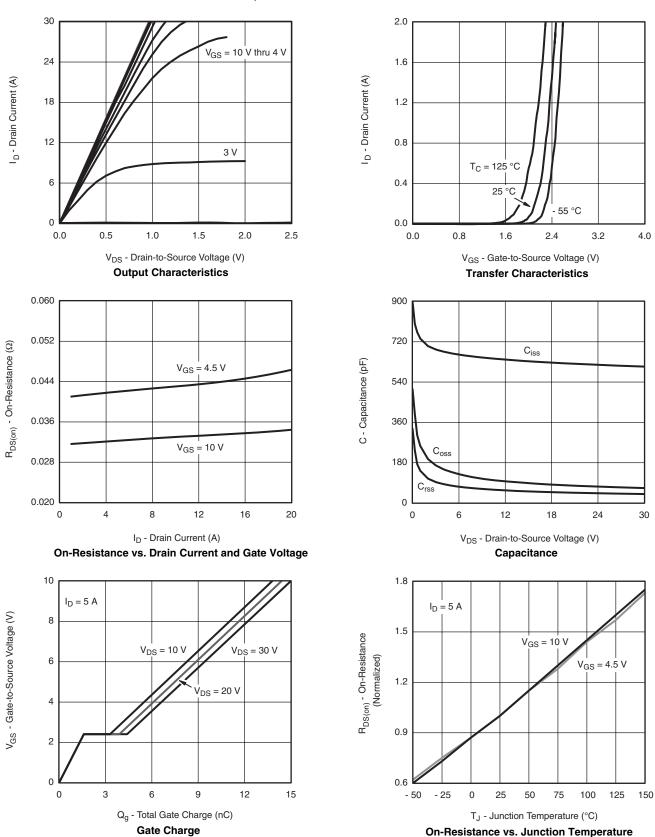
b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.



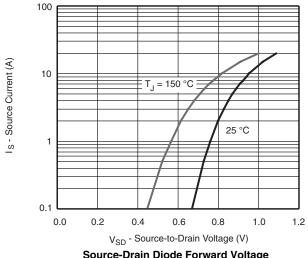


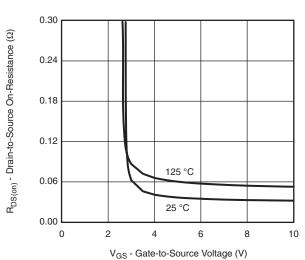


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



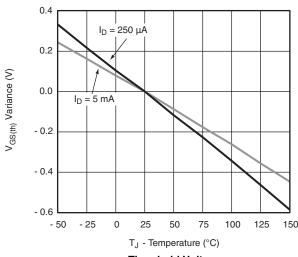
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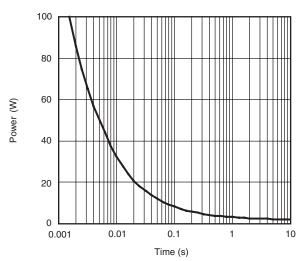




Source-Drain Diode Forward Voltage

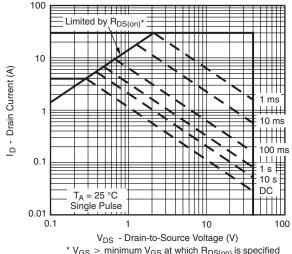






Threshold Voltage

Single Pulse Power, Junction-to-Ambient



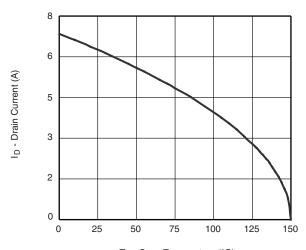
* $V_{GS} > minimum \, V_{GS}$ at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient



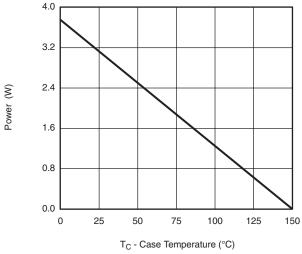


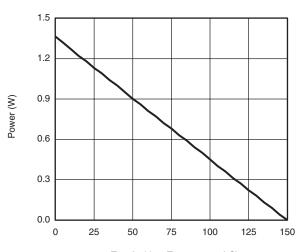
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



 $T_{\mbox{\scriptsize C}}$ - Case Temperature (°C)

Current Derating*





T_A - Ambient Temperature (°C)

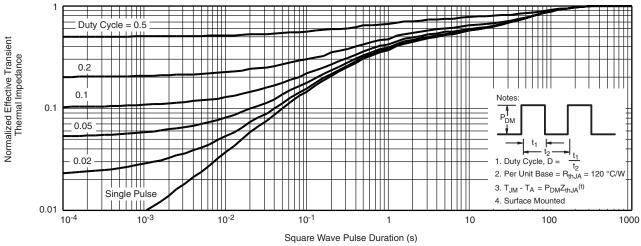
Power Derating, Junction-to-Ambient

Power Derating, Junction-to-Foot

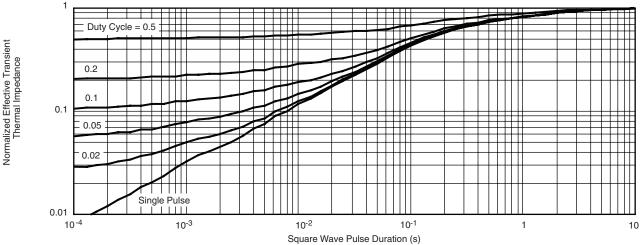
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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